

Amendments to the Specification

Please replace paragraph [0018] of the substitute specification filed February 4, 2005, with the following replacement paragraph:

-- [0018] Figs. 5A-C illustrates a process by which the quality of an image is determined and the indicator is displayed; --

Please replace paragraph [0039] of the substitute specification filed February 4, 2005, with the following replacement paragraph:

-- [0039] Referring to Figs. 5A-C, an image is captured by the camera unit 7 (FIG. 1) and displayed in the image window 19 (FIG. 3) (step S1). The microcontroller 8 executes the exposure control algorithm (step S2) and determines it has converged (step S3). If so, the first flag 23a is set to "1" indicating "stable" (step S4.1), otherwise, it is set to "0" indicating "not stable" (step S4.2). The microcontroller 8 runs a white balance algorithm (step S5) (step S6). If the algorithm converges, the second flag 23b is set to "1" indicating "stable" (step S7.1), otherwise, it is set to "0" indicating "not stable" (step S7.2). The microcontroller 8 runs a dark calibration algorithm (step S8) (step S9). If stable, the third flag 23c is set to "1" (step S10.1), otherwise, it is set to "0" (step S10.2). --

Please replace paragraph [0050] of the substitute specification filed February 4, 2005, with the following replacement paragraph:

-- The microcontroller 8 executes first, second and third routines 31, 32, 33. The first routine 31 performs the exposure control algorithm which encompasses steps S2 to S4 of the process shown in Figs. 5A-C. The second routine 32 performs the white balancing algorithm which generally covers steps S5 to S7 of the process shown in Figs. 5A-C. The third routine 33 performs the dark reference algorithm which is broadly encompasses steps S8 to S10. While performing the algorithms, the microcontroller 8 exchanges signals 34, 35, 36 with the camera unit 7. For example, the first routine 31 receives light levels and sends instructions regarding pixel integration time. The second routine 32 receives colour channel information and sends instructions to adjust gain for each colour channel. The third routine 33 receives information regarding pixel leakage and sends instructions to subtract this from the electrical signal 27. --

Please replace paragraph [0052] of the substitute specification filed February 4, 2005, with the following replacement paragraph:

-- The microcontroller 8 executes a fourth routine 40 which checks the content of the register 22 as outlined in steps S11 to S17 of the process shown in Figs. 5A-C. The fourth routine 40 produces a signal 41 which is fed to the display driver 29 for displaying the indicator 20 appropriate to the content of the register 22. --